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[0002] **Execute in place** (XIP) images are fixed position images and are built ... [0004] **NAND flash memory** typically does not support executing code in place ...

www.freshpatents.com/System-and-method-for-executing-binary-images-dt20070405ptan20070076475.php - 29k - <u>Cached - Similar pages</u>

## [PDF] The Enabling of an Execute-In-Place Architecture to Reduce the ...

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Store and Download model or an **eXecute in Place** model. ..... improve Intel NOR and **NAND flash memory** support for Linux. JOURNAL OF COMPUTERS, VOL. 3, NO. ...

www.academypublisher.com/jcp/vol03/no01/jcp03017989.pdf - Similar pages

## Flash memory - Wikipedia, the free encyclopedia

**NAND flash memory** forms the core of the removable USB interface storage devices .... most microprocessors can use NOR **flash memory** as **execute in place** (XIP) ...

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## Wiki: Flash memory

The low-level interface to **flash memory** chips usually differs from those of other ... **NAND flash** memories cannot provide **execute in place** due to their ...

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### Home Toys Article

It then loads the remainder of the boot code from the **NAND flash memory** into the system's RAM and continues the standard boot process. ...

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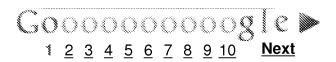
A flash memory chip having an apparatus for controlling execute-in-place (XIP) in ..... For example, a NAND flash that is a kind of serial flash having 528 ...

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#### WaferNews - Samsung drives flash technology harder

20 that it has developed the industry's first 60nm 8Gbit **NAND flash memory** device. **...** 14, in San Francisco) that Intel believes that the **execute-in-place ...** 

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8MB Single Chip Flash Disk. JUL 2000. 93-SR-002-00-8L Rev. 2.1 .... ஒர் குவுக்கும் நடிக்கு முக்கு விறும் இது கூறு கிறும் fetch and execute instructions in place (XIP) at boot time, ...

newsletter.spezial.de/pdfdata/DOC-MD2800-2810.pdf - Similar pages

# Non-volatile storage for mobile phones - Communications Engineer

NAND flash and a disk-on-chip. Battery backed RAM provides high speed random ... naturally for execute in place (XIP). Extending its use ...

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# [PDF] NAND versus NOR

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sung's OneNAND and M-Systems' mDOC (mobile disk on chip). ... The XIP model consists of NOR memory and volatile memory, likely DRAM (Fig-...

www.cs.uml.edu/ecg/wiki/blackfin/files/edn-nand-vs-nor-flash-20051013.pdf - Similar pages

## [PDF] Flash-based storage devices replace hard **disk** drives

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reduce the system susceptibility to noise. **Disk** on. **chip** products have the added capabilities of boot. and XIP, but require proprietary software and ...

www.embedded-control-europe.com/pdf/ECENov02p62.pdf - Similar pages

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Toshiba 1.8" HDD. Toshiba 0.8" HDD. M-systems Disk-On-Chip ... XIP (eXecute In Place). Concept.

Direct execution of programs in flash memory ...

kernel.kaist.ac.kr/~jinsoo/course/cs530-2004fall/slides/sslim.pdf - Similar pages

# [PDF] Versatile/PB926EJ-S FAQ v1.0

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### Disk on Module

Hi-Speed Solid State Hard Disk www.memorydepot.com

## M Systems Distributor

Source for DiskOnChip, DIP, TSOP USB, IDE & SCSI Flash Drives

# Apacer Disk on Chip (DOC)

ADC IT Series industrial Grade DOC From 256MB to 4GB, Std/Wide Temp. www.e-itx.com

http://www.google.com/search?hl=en&rls=GGLD%2CGGLD...ights=&as\_occt=any&cr=&as\_nlo=&as\_nhi=&safe=images (1 of 3)3/6/2008 2:11:16 PM

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Boot monitor v1.1 does include support for **disk-on-chip** Flash. It can be loaded in the **XIP** block and can retarget the file I/O library to **disk-on-chip** ...

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bottleneck (XIP feature). - Spansion, Intel, STM,. Atmel, SST, others. NAND for Data Store ...

OneNAND and Disk-on-Chip can take some NOR ...

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underlying concept of the mDOC (**Disk On Chip**) H3 embedded flash drive is to ... eXecute In Place (**XIP**) functionality, allowing the mDOC H3 to function as ...

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### [PDF] Diploma Thesis

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2.2.7 **Disk On Chip** (DOC). The DiskOnChip technology was developed by m-systems ..... where each cell is addressed through negated OR logic and supports **XIP** ...

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## [PDF] Running Windows CE "XIP" on the ÉlanSC400 Evaluation Platform

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on a **chip** which boasts 486/100 MHz CPU performance, fully integrated PC/AT ..... build directory as a remote **disk**. Change directory so the current directory ...

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